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The first book to focus on 3D flash memories; Provides details of flash 3D architectures which have never been published before, including a number of 3D cross sections ; Offers unique coverage of flash with Through-Silicon-Via (TSV) technology; see more benefits

3D Flash Memories | Rino Micheloni | Springer

Introduction This book walks the reader through the next step in the evolution of NAND flash memory technology, namely the development of 3D flash memories, in which multiple layers of memory cells are grown within the same piece of silicon.

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R. Micheloni (ed.), 3D Flash Memories (Springer, 2016) Google Scholar 71. K.-T. Park et al., 19.5 three-dimensional 128

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Gb MLC vertical NAND Flash-memory with 24-WL stacked layers and 50 MB/s high-speed programming, in 2014 IEEE International Solid-State Circuits Conference Digest of Technical Papers (ISSCC) , pp. 334-335, Feb 9-13, 2014
Google Scholar

3D NAND Flash Memories | SpringerLink

Y. Yanagihara et al., Control gate length, spacing and stacked layer number design for 3D-stackable NAND flash memory, in IEEE International Memory Workshop (IMW), 20-23 May 2012, pp. 1-4
Google Scholar

Reliability of 3D NAND Flash Memories | SpringerLink

R. Micheloni, L. Crippa, A. Marelli, Inside NAND Flash Memories (Springer, 2010)
Google Scholar 10. T. Mizuno et al., Experimental study of threshold voltage fluctuation due to statistical variation of channel dopant number in MOSFET's.

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3D Stacked NAND Flash Memories | SpringerLink

Category: Computers. This book walks the reader through the next step in the evolution of NAND flash memory technology, namely the development of 3D flash memories, in which multiple layers of memory cells are grown within the same piece of silicon.

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The complex example of flash memories is used to introduce and apply the methodology. It has been developed by the author based on an industrial 2-bit to 4-bit per cell flash development project. For the first time, design and cost aspects of 3D integration of flash memory are treated in this book.

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possible with 3D? Will 3D be reliable enough for enterprise and datacenter applications? These are some of the questions that this book helps answering

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by providing insights into 3D flash memory design, process technology and applications. Hardcover: 380 pages
Publisher: Springer; 1st ed. 2016 edition (June 27, 2016) Language: English
ISBN-10 ...

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Dr. Micheloni has published the following books with Springer: Solid-State-Drives (SSDs) Modeling (2017), 3D Flash Memories (2016), Inside Solid State Drives (2013), Inside NAND Flash Memories (2010), Error Correction Codes for Non-Volatile Memories (2008), Memories in Wireless Systems (2008), and VLSI-Design of Non-Volatile Memories (2005).

Inside Solid State Drives (SSDs) | Rino Micheloni | Springer

This book introduces simulation tools and strategies for complex systems of solid-state-drives (SSDs) which consist of a flash multi-core microcontroller plus

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NAND flash memories. It provides a broad overview of the most popular simulation tools, with special focus on open source solutions.

Solid-State-Drives (SSDs) Modeling - Springer

A Flash memory is a Non Volatile Memory (NVM) whose "unit cells" are fabricated in CMOS technology and programmed and erased electrically. In 1971, Frohman-Bentchkowsky developed a floating polysilicon gate transistor [1, 2], in which hot electrons were injected in the floating gate and removed

Flash Memories | Paulo Cappelletti | Springer

springer, The subject of this book is to introduce a model-based quantitative performance indicator methodology applicable for performance, cost and reliability optimization of non-volatile memories. The complex example of flash memories is used to introduce and

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apply the methodology. It has been developed by the author based on an industrial 2-bit to 4-bit per cell flash development project.

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S. Hachiya et al., Comprehensive comparison of 3D-TSV integrated solid-state drives (SSDs) with storage class memory and NAND flash memory, in 3D Systems Integration Conference (3DIC), 2015 International, 31 Aug–2 Sept 2015, pp. TS6.2.1–TS6.2.5 [Google Scholar](#)

3D Multi-chip Integration and Packaging ... - Springer

The 3D vertical flash memory has a nitride layer inside an Oxide-NitrideOxide (ONO) stack, which acts as a CT layer along the circumference of the thin poly-silicon vertical channel. Please note that each CT cell in this 3D vertical NAND memory is surrounded by the metal gates [24].

Reliability of 3D NAND Flash

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This book walks the reader through the next step in the evolution of NAND flash memory technology, namely the development of 3D flash memories, in which multiple layers of memory cells are grown within the same piece of silicon.

3D Flash Memories: Micheloni, Rino: 9789401775106: Amazon ...

This book walks the reader through the next step in the evolution of NAND flash memory technology, namely the development of 3D flash memories, in which multiple layers of memory cells are grown within the same piece of silicon.

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Inside NAND Flash Memories - GBV

Digital photography, MP3, digital video, etc. make extensive use of NAND-based Flash cards as storage media. To realize how much NAND Flash memories pervade every aspect of our life, just imagine how our recent habits would change if the NAND memories suddenly disappeared.

Inside NAND Flash Memories: Micheloni, Rino, Crippa, Luca ...

This book walks the reader through the next step in the evolution of NAND flash memory technology, namely the development of 3D flash memories, in which multiple layers of memory cells are grown

3D flash memories (eBook, 2016) [WorldCat.org]

The complex example of flash memories is used to introduce and apply the

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methodology. It has been developed by the author based on an industrial 2-bit to 4-bit per cell flash development project. For the first time, design and cost aspects of 3D integration of flash memory are treated in this book.

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